

DEPARTMENT OF ELECTRICAL ENGINEERING NATIONAL INSTITUTE OF TECHNOLOGY, RAIPUR

Phone: 0771-2254200, Fax: 0771 - 2254600

No/NITRR/EED/2023/249 09/06/2023

Applications are invited for a **walk in interview on 26/06/2023 (11 AM)** for the post of a **Junior Research Fellow** in the Department of Electrical Engineering, National Institute of Technology, Raipur, Chhattisgarh, India, under research project entitled "Detection of Data Attacks and Design of resistance mechanism in smart grids" sanctioned by DST-ICPS.

ELIGIBILITY: ME/M Tech or equivalent in Electrical, Computer Science, Information Technology or related/allied fields selected through a process described through any one of the following

- a. Scholars who are selected through National Eligibility Tests CSIR-UGC NET including lectureship (Assistant Professorship) and GATE.
- b. The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc.

Monthly Emoluments: 25000+HRA(as per institute norms) INR

Desirable Qualification: Knowledge of basic programming skills in Python/MATLAB®/C/C++.

Understanding of power system or cyber security.

Duration of the project: Till September 2023

Terms and conditions:

- 1. No TA/DA is permissible for appearing in the interview.
- 2. The appointment will be till **30/09/2023**. However, the candidate may be discontinued any time by giving a one-month prior notice if the performance is not found satisfactory. Selected candidates will not be permitted to claim for any regular/part time appointments in this institute or any other institutes during this period.
- 3. The above position is purely temporary, and continuation of the fellowship is subject to satisfactory performance and review and availability of fund under project.
- 4. The appointment shall be co-terminus with the project and will be governed as per the Terms and Conditions of funding agency and Norms of NIT Raipur.
- 5. Candidates are required to appear for the interview with photocopy of the application form, two passport size photos, curriculum vitae, testimonials and all relevant original certificates with a one set of self-attested photocopies of the certificates.

Interested applicants may e-mail the duly filled application form, scanned documents pertaining to educational qualifications, age proof, GATE/ NET qualification to the undersigned with e-mail subject marked as "Application for Junior Research Fellow (ICPS-DST)" to (sghosh.ele@nitrr.ac.in) by 24/06/2023. Selected candidates shall have to sign an agreement as per the norms of NIT Raipur at the time of joining. For details please contact: Dr. S Ghosh, Associate Professor, Electrical Engineering Department, NIT Raipur (email:sghosh.ele@nitrr.ac.in)



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ICPS (DST) project entitled "Detection of Data Attacks and Design of resistance mechanism in smart grids"

Application for Junior Research Fellow

	rth 06/9/2022	Email	l ID:			
4. Age as on 06/9/2022 : 5. Nationality : 6. Category : 7. Sex : 8. Complete Academic Record (Starting from X standard) (attach certificate) S. No. Degree Specialization University/Institute Year of %	06/9/2022	:				
	•	: : : : d (Starting)	from X standar	d) (attach certi	ficates)	
	Degree Special	lization U1	niversity/Institute	Year of Completion	% Mark Divis	

- 9. Areas of Interest:
- 10. Publications/conference participation (National/International) (Attach proofs):
- 11. Research experience:
- 12. Whether GATE/CSIR-UGC NET qualified (Attach proof):
- 13. Any Other Relevant Information:

UNDERTAKING

I understand that it is a contractual engagement on purely temporary basis for the Project. I declare that the above are correct to the best of my knowledge and belief. I understand that action can be taken against me if these are found to be incorrect. I agree to all the terms and conditions of the associated advertisement, DST India and NIT Raipur.

Date	:

Place : Signature of Applicant