



राष्ट्रीय प्रौद्योगिकी संस्थान रायपुर

NATIONAL INSTITUTE OF TECHNOLOGY RAIPUR

(An Institute of National Importance)

G.E. Road, Raipur - 492010 (CG)

Phone: (0771) 2252700

Email: [registran@nitrr.ac.in](mailto:registran@nitrr.ac.in)

Website: [www.nitrr.ac.in](http://www.nitrr.ac.in)

No./ NITRR/R-1/Adv/2017/225

Raipur, Dated 07/07/2017

## **WALK-IN-INTERVIEW**

Applications are invited from research oriented candidates having B.Tech/M.Tech/PhD degree in Electronics & Communication Engineering for purely temporary positions as Faculty/Project Assistant under the project titled "**Special Manpower Development Project - chips to System Design (SMDP-C2SD)**" sponsored by Ministry of Electronics and Information Technology, Govt. of India. Interested candidates may visit "[www.nitrr.ac.in](http://www.nitrr.ac.in)" for further detail and appear for written test/interview on **27/07/2017 at 09.00 AM** in the Department of Electronics and Telecommunication Engineering, NIT Raipur. The candidates having knowledge in the field of Microelectronics/VLSI with experience in CAD tools and Chip design/Linux will be preferred.

**-Sd-  
REGISTRAR**



राष्ट्रीय प्रौद्योगिकी संस्थान रायपुर  
NATIONAL INSTITUTE OF TECHNOLOGY RAIPUR  
(Institute of National Importance)  
G.E. Road, Raipur – 492010 (C'G)

Phone: (0771) 225 42 00  
Fax: (0771) 225 46 00  
Email: director.nitr@rediffmail.com  
Website: www.nitr.ac.in

NO./NITRR/R-1/Advt./2017/225

Raipur, Dated 07/07/2017

### WALK- IN- INTERVIEW

Recruitment of the Project Staff through Walk-in interview (Temporary Faculty and Project Assistant) under the following MeitY-Sponsored Project.

#### *“Special Manpower Development Project - Chips to System Design (SMDP- C2SD)”*

Interested candidates may attend the interview with original and attested copies of all academic and experience certificates (if any) and testimonials on **27/07/2017 at 09.00 AM** in the **Department of Electronics and Telecommunication Engineering, NIT Raipur**

#### Details about the position(s):

S.No.	Post	Qualification	Duration	Consolidated Salary
1	Guest/Temporary Faculty No of posts: 1	ME/MTech/PhD in the field of Microelectronics/VLSI with experience in CAD tools and Chip design/Linux	Initially for the period of 1 year which may be extended as per the terms of SMDP-C2SD project.	Rs 40,000/- (Per month)
2	Project Assistant No of posts: 1	B. Tech/ M. Tech in Electronics & Comm. Engg. with experience in CAD tools Networking/Linux	As above	Rs. 20,000/- (Per month)

#### Note:-

- Candidate having relevant experience in VLSI CAD tools (hands-on), Linux and computer networking and related areas will be preferred.

### **Terms and conditions:**

1. The above position is purely contractual, initially for 1 year and may be renewed further.
2. No TA/DA will be paid for attending the interview.
3. Candidates have to appear for walk-in interview/written test with bio-data, passport photos, original certificates and attested photocopies of all certificates, mark sheets, degrees and testimonials.
4. Bio-data format is being provided with this notification. The candidates are required to bring duly filled in bio-data format along with photocopies of required documents.
5. Reporting time at the Department of Electronics and Telecommunication, NIT Raipur for the Interview/written test: **27/07/2017, 09:00 A.M.**
6. Candidates reporting late will not be entertained.
7. The institute reserves the right to fill or not to fill any or all the posts.

**Dr. Shrish Verma (shrishverma@nitrr.ac.in) & Dr. B Acharya (bacharya.etc@nitrr.ac.in)**  
**Chief & Co-Chief Investigators, SMDP-C2SD**  
**E & TC, National Institute of Technology Raipur**  
**G.E. Road, Raipur**  
**Chhattisgarh – 492010**  
**India**



**राष्ट्रीय प्रौद्योगिकी संस्थान रायपुर**  
**NATIONAL INSTITUTE OF TECHNOLOGY RAIPUR**  
(Institute of National Importance)  
G.E. Road, Raipur – 492010 (C'G)

Phone: (0771) 225 42 00  
Fax: (0771) 225 46 00  
Email: [director.nitr@rediffmail.com](mailto:director.nitr@rediffmail.com)  
Website: [www.nitr.ac.in](http://www.nitr.ac.in)

1. Post Applied for : Guest Lecturer / Project Assistant\_\_\_\_\_

2. Name of the Candidate (BLOCKLETTER):

3. Father's Name(BLOCKLETTER):

4. Mother's Name(BLOCKLETTER):

5. (a) Date of Birth:(DD/MM/YYYY)\_\_\_\_\_ (b)Sex(Male/Female/Other):\_\_ \_

(c) Marital Status: Married/ Single (d)Category: SC/ST/OBC/PWD/Open

6. Previous Research experience: \_\_\_\_\_

7. Publication(s), if any: \_\_\_\_\_

8. GATE/ NET (if any):Qualified (Yes/No):Score: \_\_\_\_\_ Rank:\_\_\_\_\_

Specialization: \_\_\_\_\_ Year: \_\_\_\_\_

9. Academic Qualification: (Starting from Standard 10 or equivalent Examination)

Name of Exam Passed	Name of the School/College/Institute/ University	Year of Passing	Discipline/ Specialization	Percentage of Marks/ CGPA

10. (a) Address for

Communication:

(BLOCKLETTER)

Paste here a recent  
Passport size  
Photograph

(b) Contact No (Mob)

(c) E-mail ID

11. Name of the Referee with email \_\_\_\_\_

---

12. Experience on Cadence/ \_\_\_\_ Synopsys/ \_\_\_\_ Mentor/ \_\_\_\_ Linux / \_\_\_\_ Others/ \_\_\_\_\_ (in years)

I do here by declare that the information furnished in this application is true to the best of my knowledge and belief. If selected, I promise to abide by the rules and regulations of the Institute.

Date:

Note- If any information is required, extra A4 sheet may be attach.

Place:

**Full Signature of the Applicant**

## **ELIGIBILITY**

1. M.Tech./M.E./ equivalent in Microelectronics & VLSI / or E & TC with strong exposure to VLSI Design, CAD Tools with at least 6.5 CGPA or 60 percent marks aggregate in all the examination with at least first division in from graduation level onward from a recognized technical institute or university/ university as a Full-time program.
2. B. Tech/B.E./ in E & TC/Electronics and relevant discipline with at least 7.5 CGPA or 70 percent marks in aggregate from recognized technical institute or university/as a Full-time program.
3. **Desirable:** Candidate having relevant experience in VLSI CAD tools (hands-on), Linux and computer networking and related areas will be preferred.

## **APPLICATION CHECK LIST (AT THE TIME OF INTERVIEW)**

The completed application (duly signed) must be produced with following self-attested documents at the time of interview.

- i) Photocopy of marks sheet/Grade card of the secondary (10), higher secondary (10+2),
- ii) Photocopy of the certificate/provisional certificate of B.E/B.Tech, M.E/M. Tech, PhD examination
- iii) Photo copy of proof of date of birth
- iv) No-objection certificate from the present employer, if applicable.